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Atty. Docket No. NLMI.P212

PATENT

IN THE UNITED STATES PATENT OFFICE

In Re Patent Application of

First Named Inventor: Srinivasan, Varadarajan

Application No.: 10/613,892

Filed: 7/2/2003

For: METHOD AND APPARATUS FOR
TERMINATING SELECTED TRAFFIC FLOWS

Examiner:

Art Unit: 2661

Confirmation 6907
No.

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INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.97

Sir:

Enclosed is an Information Disclosure Citation Form 1449/PTO together with a copy of each reference cited therein, excluding U.S. Patents and Published U.S. Patent Applications. It is respectfully requested that the cited references be considered and that the enclosed copy of the Form 1449/PTO be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant.

This Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

 X 37 C.F.R. §1.97(b).

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 Authorization is hereby given to charge Deposit Account No. 501914 for the fee of \$180.00 under 37 C.F.R. §1.17(p) for the submission of this Information Disclosure Statement; or

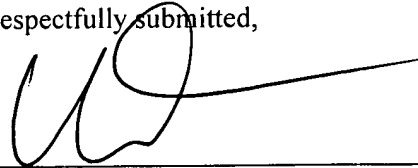
 A statement pursuant to 37 C.F.R. §1.97(e) is attached hereto.

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Pursuant to 37 C.F.R. 1.97(h), the submission of this Information Disclosure Statement is not to be construed as an admission that the information cited in this statement is material to patentability.

The Commissioner is hereby authorized to charge any fee deficiency in connection with this submission to Deposit Account No. 501914.

Respectfully submitted,

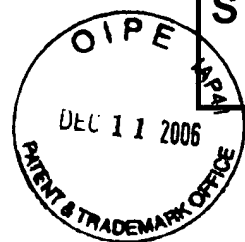


William L. Paradice, Reg. No. 38,990
Tel. 408-236-6646

Date

12/7/2006

Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT Page 1 of 5	Application No.	10/613,892
	Filed	7/2/2003
	First Inventor	Srinivasan, Varadarajan
	Art Unit	2661
	Examiner	
	Atty. Docket No.	NLMI.P212



Non Patent Literature Documents		
Examiner Initials	Name of Author, Title of Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country where Published	Translation
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	CSIX-L1:Common Switch Interface Specification-L1, published by CSIX, August 5, 2000(8/5/2000) , 72 pgs.	

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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw a line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.

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	Implementation of ATLASI: a Single-Chip ATM Switch with Backpressure, IEEE Hot Interconnects IV Symposium Proceedings, Stanford, CA, Pub. August 13-15, 1998, 12 pgs.	
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	Start-time Queuing: A Scheduling Algorithm for Integrated Services Packet Switching Networks, Pawan Goyal, Harrick M. Vin and Haichen Cheng, Distributed Multimedia Computing Laboratory, Dept. Computer Sciences, University of Texas at Austin, Pub. 1996, 12	

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	Vitesse Announces Industry's First OC-48c Traffic Management Engine, Vitesse Semiconductor Corp., Sept. 28, 2000 (9/28/00), 2 pgs.	
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	WF2Q: Worst-case Fair Weighted Fair Queueing, Jon C.R. Bennett, Hui Zhang, Pub. Date Unknown, 9 pgs.	
	What is a Network Processor?, Vitesse Semiconductor Corp., Pub Date Unknown, 4 pgs.	
	Wire Speed Quality of Service Over Ethernet, Switchcore, Pub. May 8, 2000 (5/8/00), 19 pgs.	

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	Zettacom: Hurry Up and Wait, www.lightreading.com, Pub. Nov. 29, 2000 (11/29/00), 2 pgs.	

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